WEST Search History

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DATE: Saturday, December 24, 2005

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		DB=JPAB; PLUR=YES; OP=ADJ	
☐	L25	L24 and (pad$ or pin$ or terminal$)	21
☐	L24	L23 and (IC$ or integrated circuit$ or chip$)	46
☐	L23	(semiconductor or memory) and package adj2 (form or format)	87
		DB=PGPB,USPT; PLUR=YES; OP=ADJ	
☐	L22	L14 and package adj2 (form or format)	20
☐	L21	L14 and package adj2 (form or format or configuration)	46
☐	L20	L17 and (wire$ or bond$)	45
☐	L19	L18 and (BGA or ball grid array)	11
☐	L18	L14 and (TSOP or thin small outline package)	63
☐	L17	L16 and control near2 (signal$ or pulse$ or command$ or terminal$)	70
☐	L16	L15 and signal$	79
☐	L15	L14 and (TSOP or BGA)	80
☐	L14	L13 and (arrangement or lay-out or layout or configuration)	483
☐	L13	L12 and (package or housing)	523
☐	L12	L11 and (command$ or instruction$) near4 (pin$ or terminal$ or pad$)	1414
☐	L11	L10 and address$ near3 (pin$ or terminal$ or pad$)	6080
☐	L10	L3 and (data or I/O or IO) near3 (pin$ or terminal$ or pad$)	20723
☐	L9	L8 and control near2 signal	27
☐	L8	L7 and (arrangement or lay-out or layout or configuration)	29
☐	L7	L6 and (package or housing)	34
☐	L6	L5 and (command$ or instruction$) near5 pad$	105
☐	L5	L4 and address$ near2 pad$	513
☐	L4	L3 and (data or I/O or IO) near2 pad$	3688
☐	L3	L1 and (IC$ or integrated circuit$ or chip$)	128052
☐	L2	L1 and (IC$ or integrated circuit$)	95411
☐	L1	semiconductor and memory	194753

END OF SEARCH HISTORY